

FIG. 1

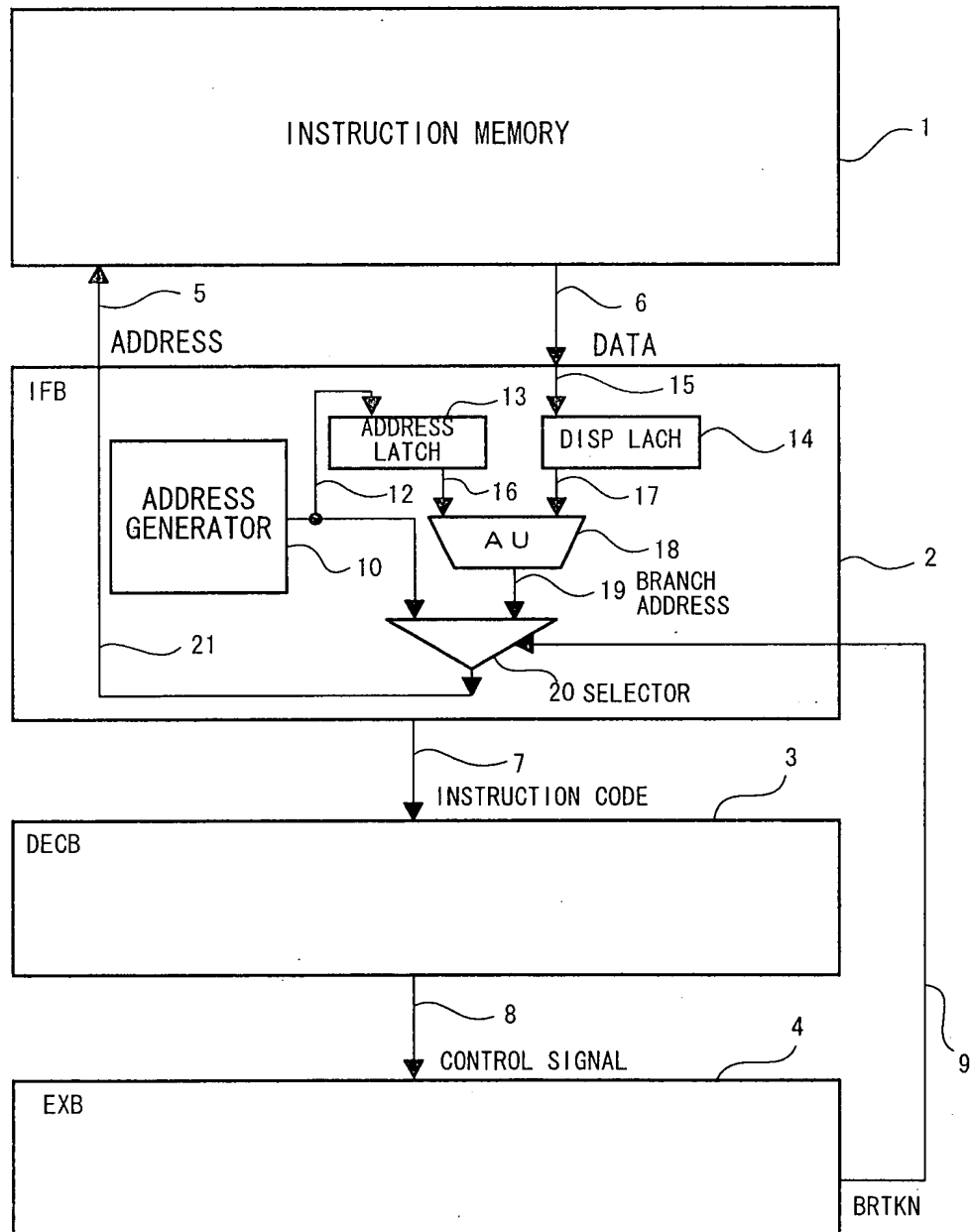


FIG. 2

	a	b	c		d	e	f
ADR	0000	0002	0004	0022	0024		
IF	SUB D2, D1	BEQdisp8	AND D0, D1	MOV D2, A0			
DEC		SUB D2, D1	BEQdisp8	MOV D2, A0			
EX			SUB D2, D1	BEQdisp8	MOV D2, A0		
BRTKN							

FIG. 3

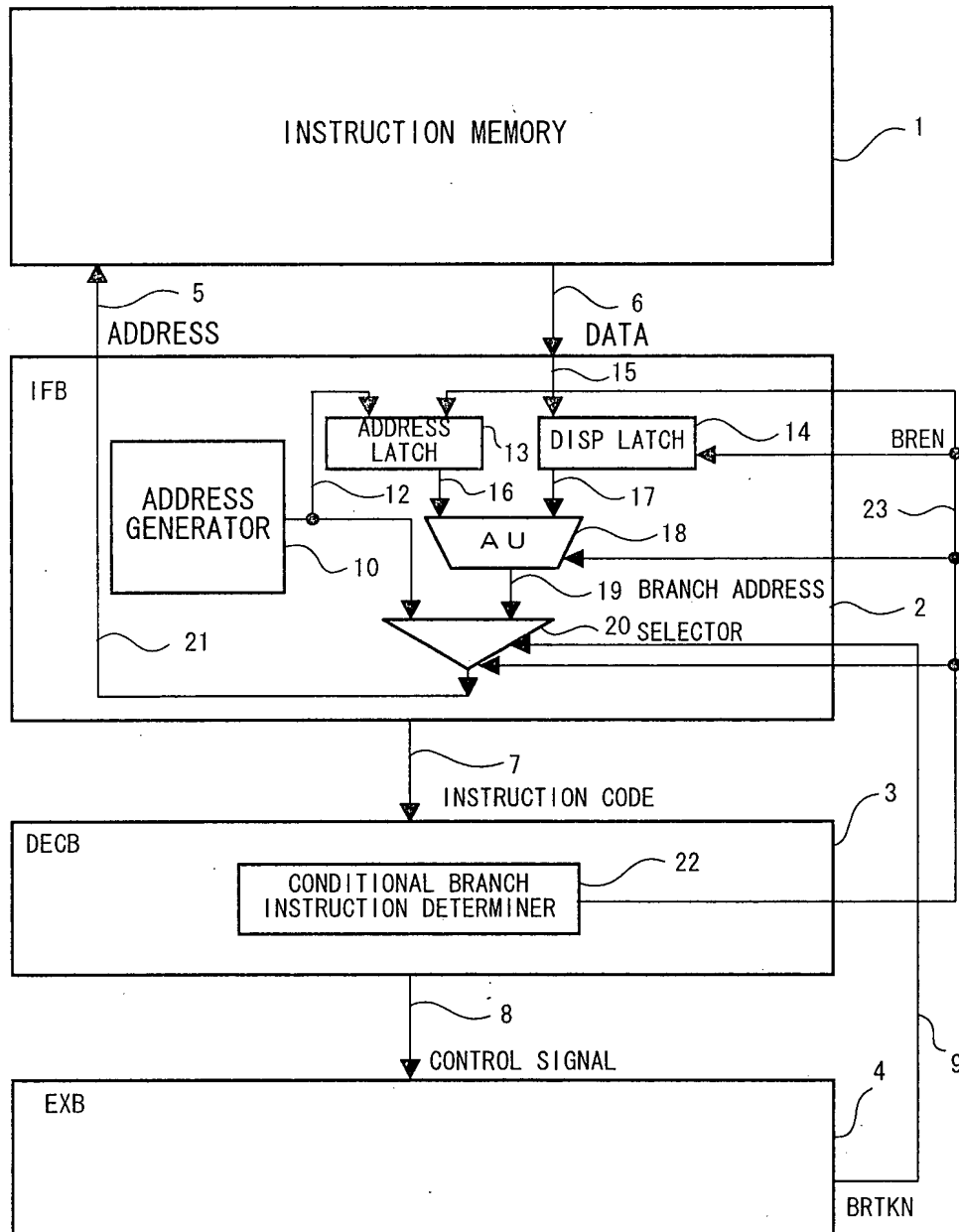


FIG. 4

	a	b	c		d	e	f
ADR	0000	0002	0004	0022	0024		
IF	SUB D2, D1	BEQdisp8	AND D0, D1	MOV D2, A0			
DEC		SUB D2, D1	BEQdisp8	MOV D2, A0			
EX			SUB D2, D1	BEQdisp8	MOV D2, A0		
BRTKN							
BREN							

FIG. 5

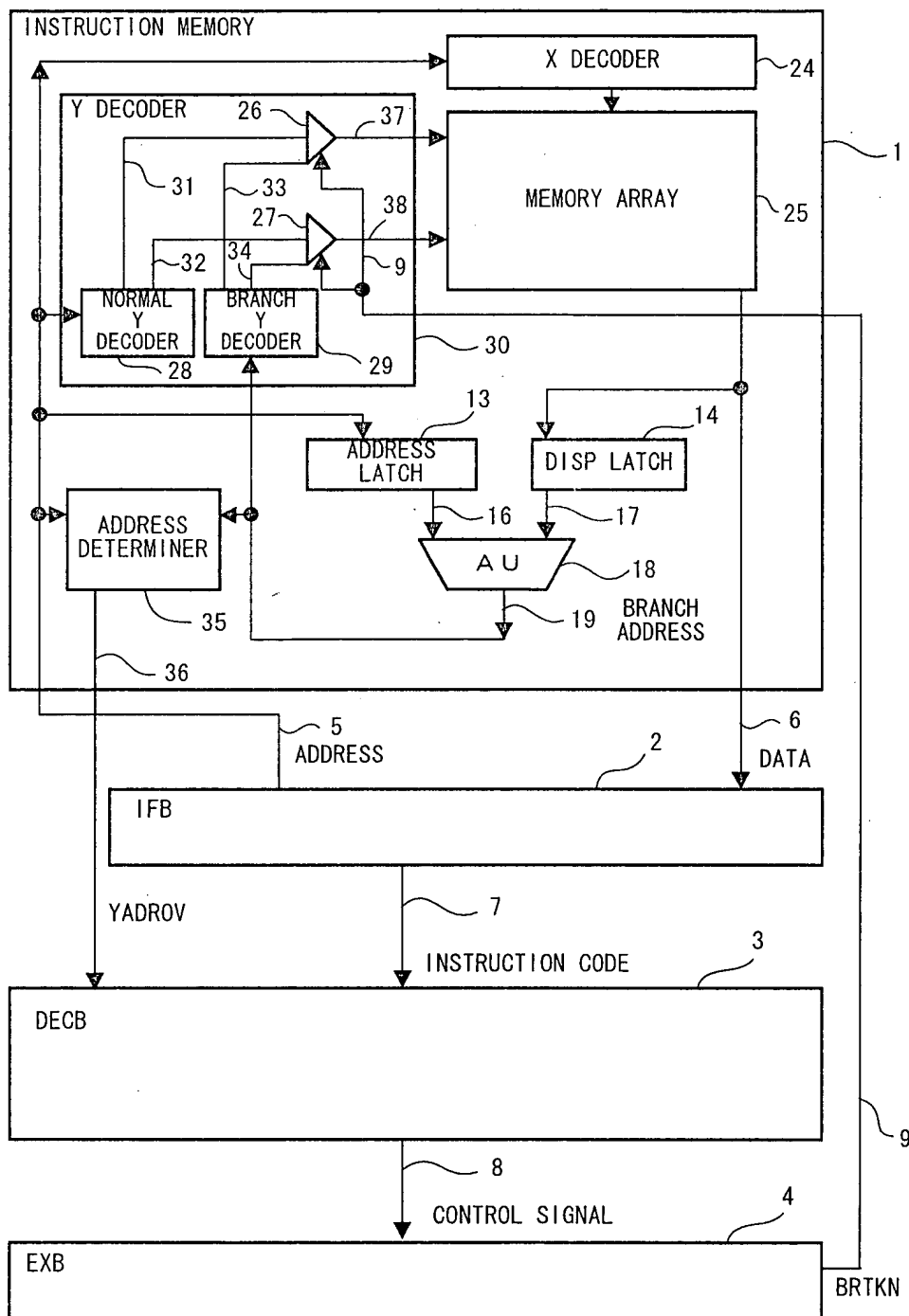


FIG. 6

	a	b	c	d	e	f
ADR	0000	0002	0004	0024		
XDEC	00	00	00	00		
YDECN	00	02	04	24		
YDECB			22			
BRTKN						
XDECP						
YDECP						
31						
32						
33						
34						
37						
38						
IF	SUB D2, D1	BEQdisp8	MOV D2, A0			
DEC		SUB D2, D1	BEQdisp8	MOV D2, A0		
EX			SUB D2, D1	BEQdisp8	MOV D2, A0	

FIG. 7

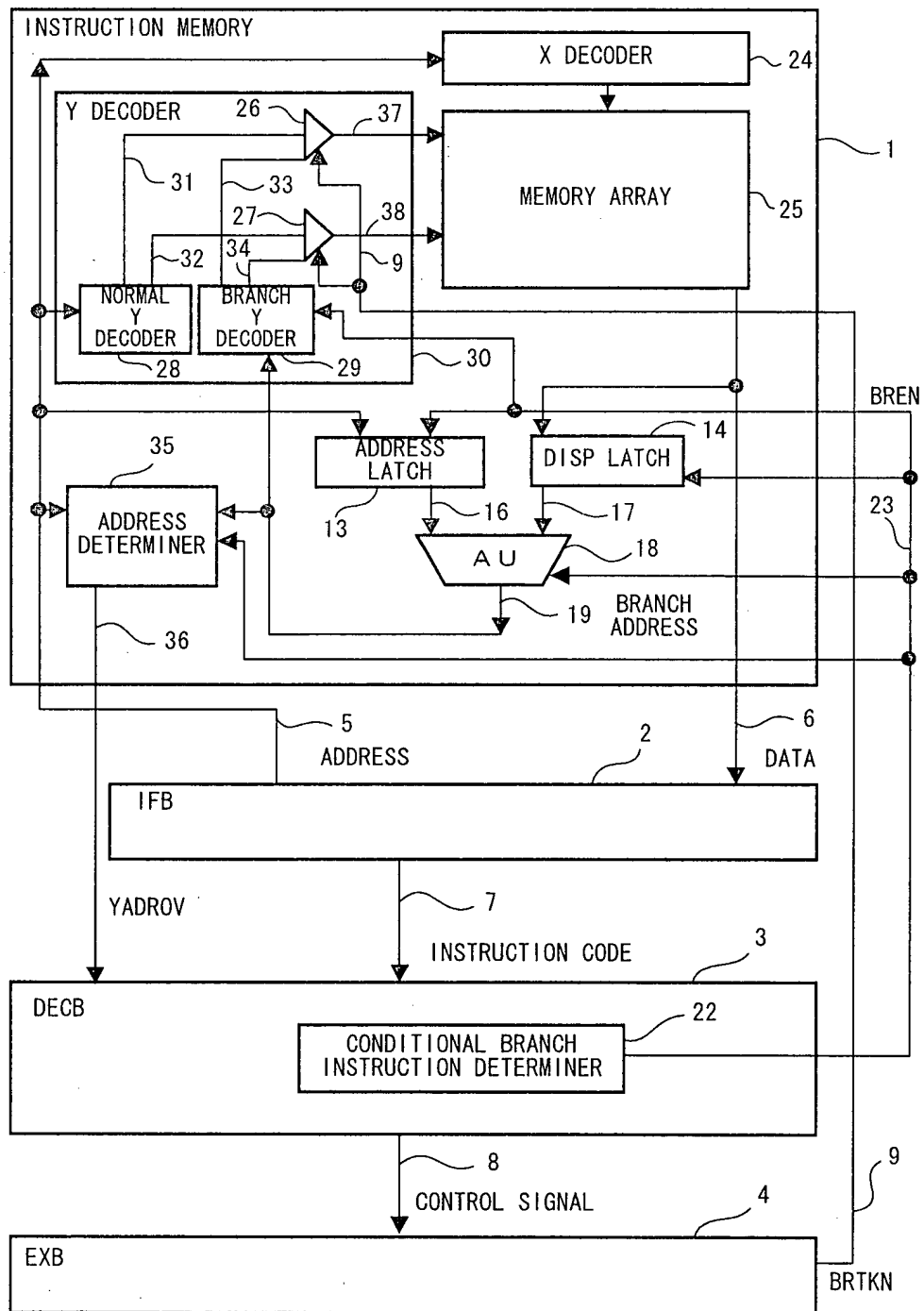


FIG. 8

	a	b	c	d	e	f
ADR	0000	0002	0004	0024		
XDEC	00	00	00	00		
YDECN	00	02	04			
YDECB			22			
BRTKN						
XDECP						
YDECP						
31						
32						
33						
34						
37						
38						
IF	SUB D2, D1	BEQdisp8	MOV D2, A0			
DEC		SUB D2, D1	BEQdisp8	MOV D2, A0		
EX			SUB D2, D1	BEQdisp8	MOV D2, A0	
BREN						

FIG. 9

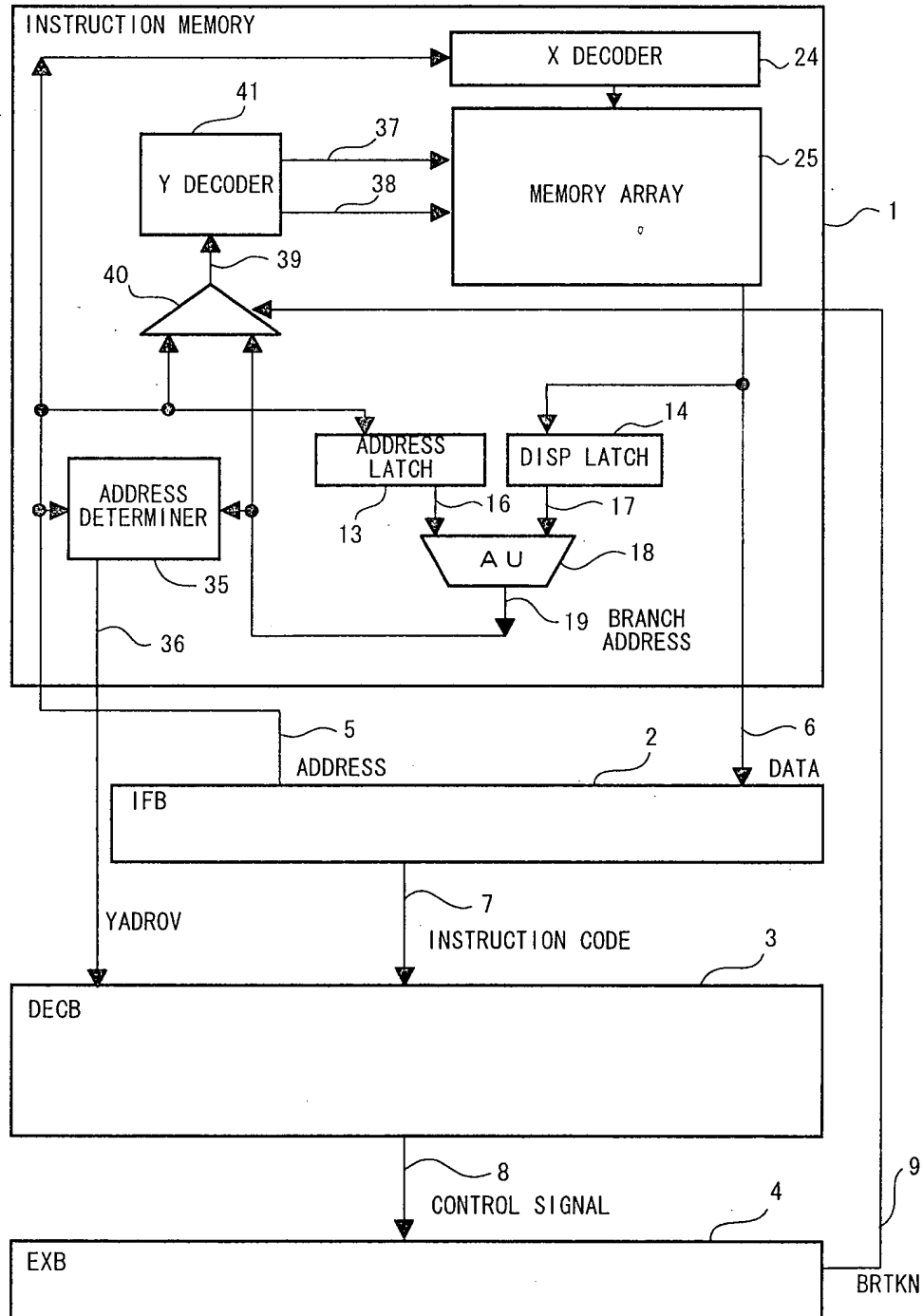


FIG. 10

	a	b	c	d	e	f
ADR	0000	0002	0004	0024		
XDEC	00	00	00	00		
YDECN	00	02	04			
YDECB			22			
BRTKN						
39	00	02	04	02		
XDECP						
YDECP						
37						
38						
IF	SUB D2, D1	BEQdisp8		MOV D2, A0		
DEC		SUB D2, D1	BEQdisp8	MOV D2, A0		
EX			SUB D2, D1	BEQdisp8	MOV D2, A0	

FIG. 11

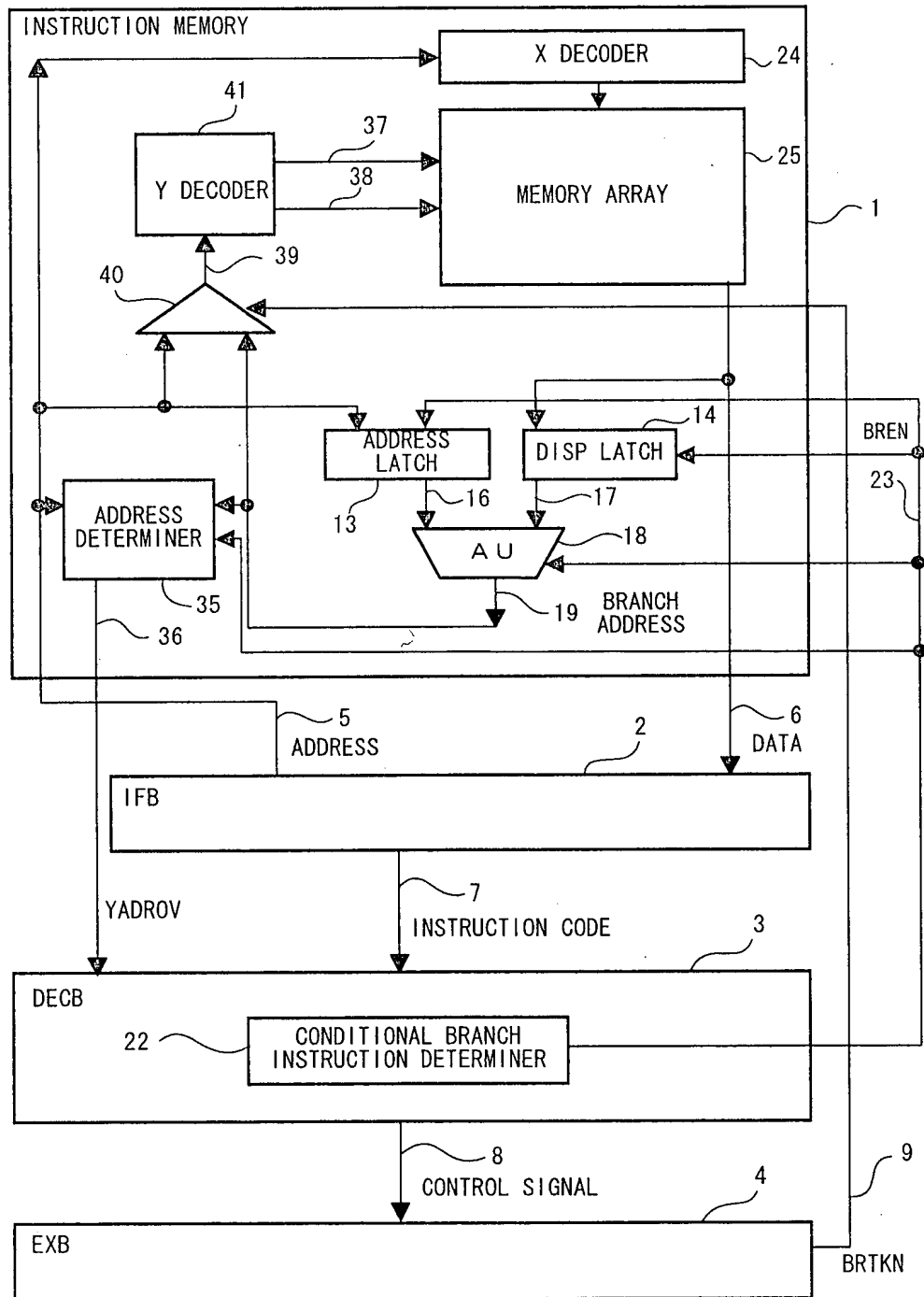


FIG. 12

	a	b	c	d	e	f
ADR	0000	0002	0004	0024		
XDEC	00	00	00	00		
YDECN	00	02	04			
YDECB			22			
BRTKN						
39	00	02	04	22		
XDECP						
YDECP						
37						
38						
IF	SUB D2, D1	BEQdisp8	MOV D2, A0			
DEC		SUB D2, D1	BEQdisp8	MOV D2, A0		
EX			SUB D2, D1	BEQdisp8	MOV D2, A0	
BREN						

FIG. 13
PRIOR ART

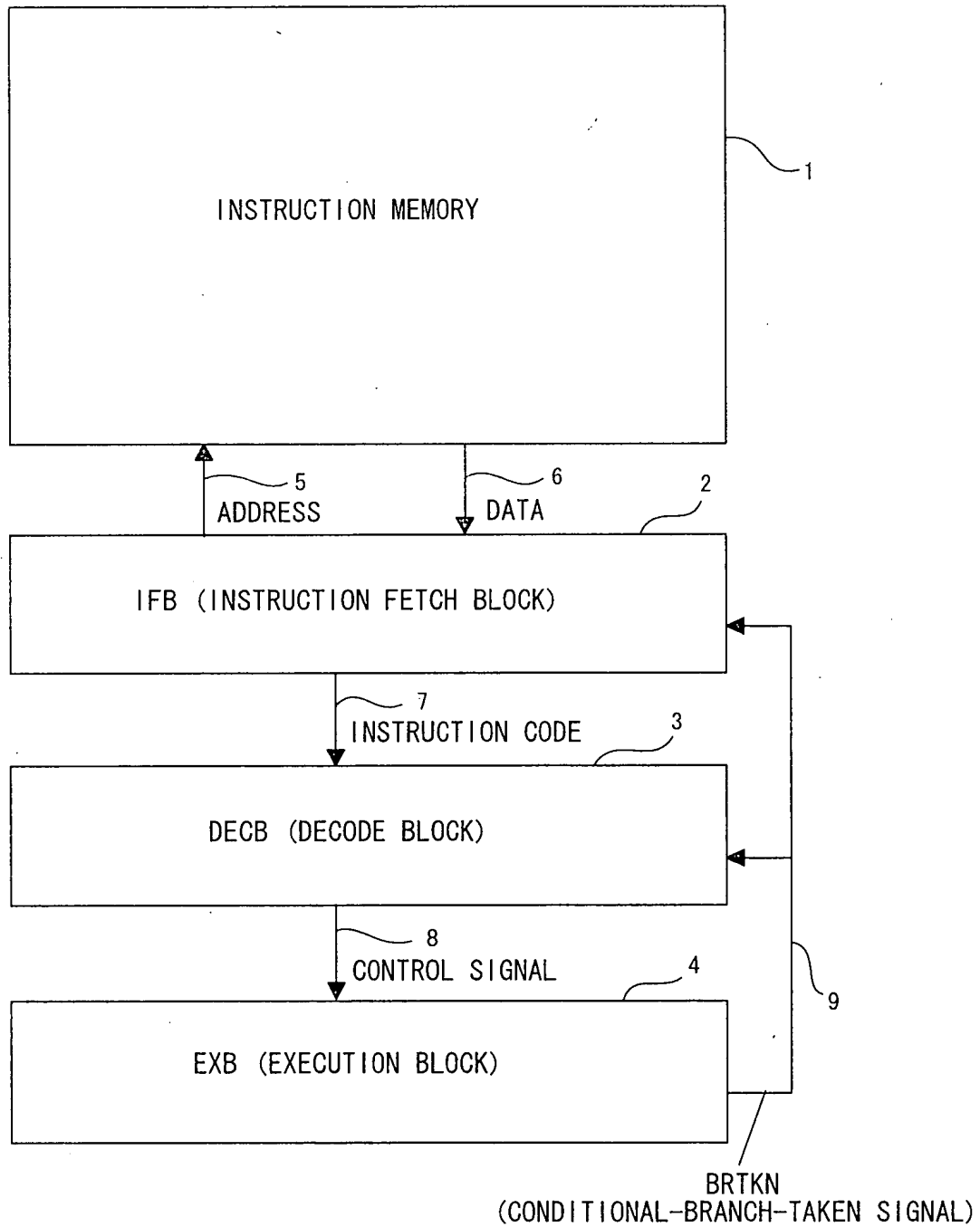


FIG. 14

PRIOR ART

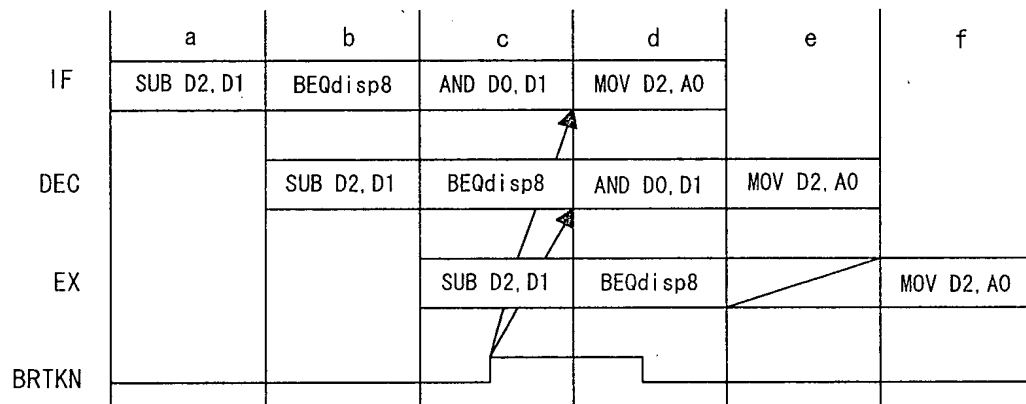


FIG. 15

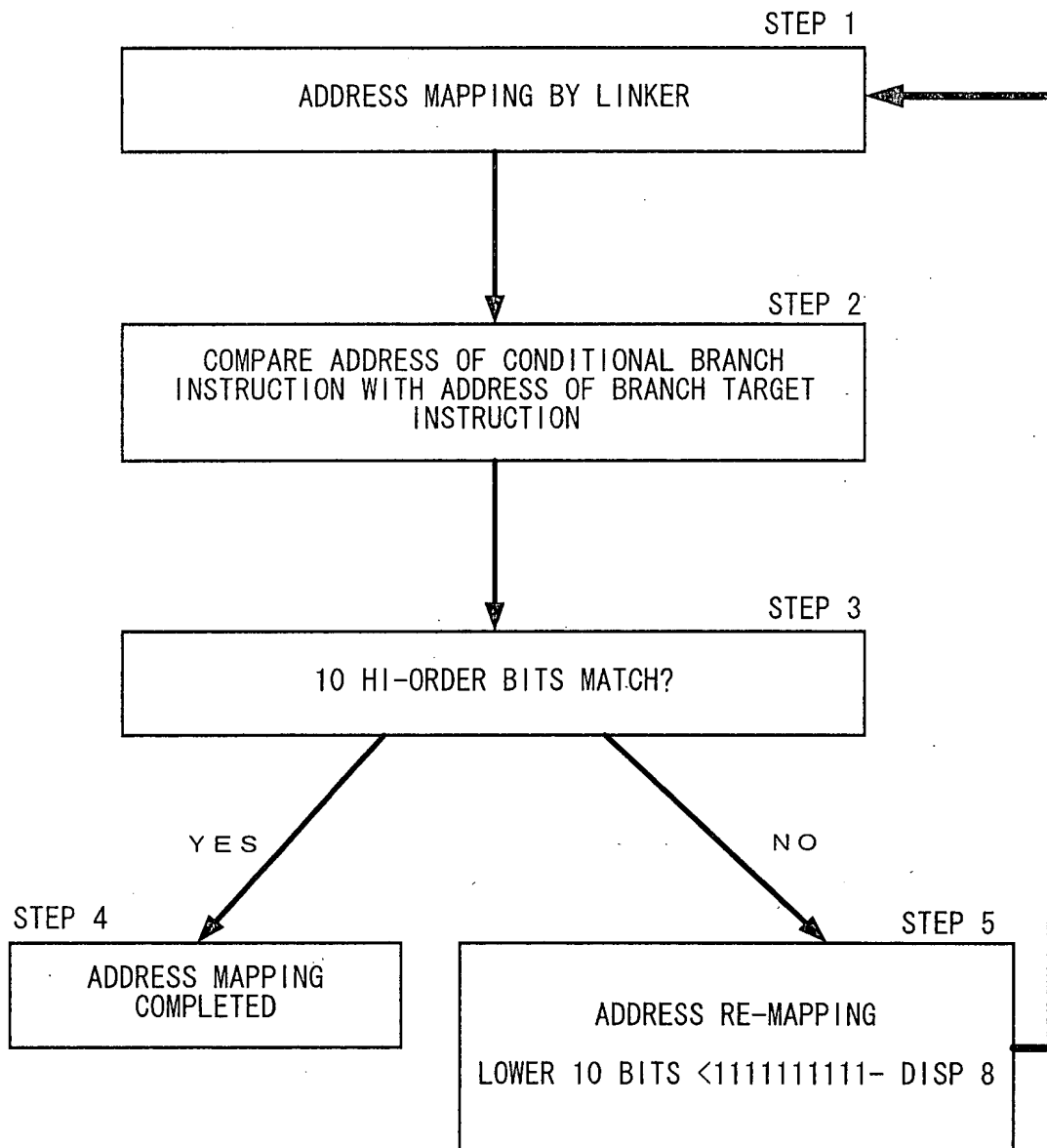


FIG. 16

a0:0000 0000 0011 1000 0000

a1:0000 0000 0011 1000 0010

a2:0000 0000 0100 0000 0000

BEQ' disp8
AND D0, D1
MOV D2, A0

FIG. 17

a0:1000 0000 0000 0000 0000
a1:1000 0000 0000 0000 0010
a2:1000 0000 0000 1000 0000

BEQ disp8
AND D0, D1
MOV D2, A0